Atty. Docket No.: 006915 USA D03/FEP/P3I/PJT RW Ref. No.: APM/007-04-DV1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)
Amir Al-Bayati, et al.	) Group Art Unit: 2812
Entitled: SEMICONDUCTOR ON INSULATOR VERTICAL TRANSISTOR FABRICATION AND	)
DOPING PROCESS	) Examiner: Alexander Ghyka
Serial No.: 11/901,969	)
Filing Date: September 18, 2007	)

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97(c)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 CFR 1.56, the references listed below and on the attached form PTO-1449 are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. A copy of each foreign patent document and/or non-patent literature is enclosed.

This information is being submitted subsequent to the later of three months after the filing date of the present application or the mailing of the first Office Action on the merits, but before the mailing of a final action or the notice of allowance. Please see the attached USPTO Fee Transmittal for fee payment.

It is respectfully requested that the Examiner indicate consideration of the cited references by returning a copy of the attached form PTO-1449 with initials or other appropriate marks.

Respectfully submitted,

asher undel-

Dated:

Robert M. Wallace Reg. No. 29,119

Attorney for Applicants Customer No. 000044843

Robert M. Wallace Law Office of Robert M. Wallace 2112 Eastman Avenue, Suite 102 Ventura, CA 93003 (805) 644-4035